

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Yasuhiko KASAMA et al.

Conf. 9839

Application No. 10/577,526

Group 2811

Filed March 16, 2007

Examiner Trang Q. Tran

COMPLEMENTARY MISFET AND INTEGRATED CIRCUIT

AMENDMENT

Assistant Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

September 9, 2009

Sir:

In response to the Official Action of June 9, 2009,
please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing
of claims which begins on page 2 of this paper.

Remarks begin on page 6 of this paper.